

**Amendments to the Specification:**

*Please replace paragraph [0011] with the following amended paragraph:*

[0011] Protocol and signaling for the system bus 16 is substantially different than the protocol and signaling for each PCI-Express bus 72. Without PCI-Express bridge 58, PCI devices 64, 66, 68 and 70 could not communicate with, for example, microprocessor 10. PCI-Express bridge 58 acts as a buffer/translator between a PCI-Express device and devices coupled to system bus 16 and memory bus 22. PCI Express bridge 58 translates PCI-Express formatted data into data formatted for transmission over system bus 16 and memory bus 22, and vice versa.